

Appl. No. 10/708,330
Amdt. dated July 29, 2005
Reply to Office action of May 04, 2005

REMARKS/ARGUMENTS

Claims 1-4, 6-8, 9-12, and 14-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kodate U. S. Patent No. 5,748,266 in view of Rho et al U. S. Patent No. 6,862,050, and further in view of Hirataka et al U.S. Patent Application Publication No. 2002/0109815. Claims 5 and 13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kodate in view of Rho et al, and Hirataka et al U.S. as mentioned above, and further in view of Miyazaki et al U.S. Patent Application Publication No. 2002/0171800.

1. Rejections of Claims 1-4, 6-8, 9-12, and 14-16 under 35 U.S.C. 103(a):

Claims 1-4, 6-8, 9-12, and 14-16 are rejected under 35 U.S.C. 103(a) for reasons of record, as recited on pages 2-5 of the above-indicated Office action.

Response:

Claim 1 has been amended to add the limitation that a plurality of scan lines, a plurality of common electrodes, and a plurality of common electrode pads are formed on an upper surface of the lower substrate *simultaneously*, wherein the common electrodes are used for transmitting a common voltage *and are positioned at the same plane as a plane where the common electrode pads are positioned on the lower substrate*, and the common electrode pads are electrically connected to the common electrodes. Claim 9 has been amended to add the limitation that a plurality of common electrode pads are electrically connected to the common electrodes, wherein *the common electrode pads and the common electrodes are positioned at a same plane on the lower substrate*. No new matter is introduced.

Kodate discloses an active LCD that a common electrode (30) is positioned on the bottom surface of the upper substrate covering the spacers, and it contacts with the storage capacitance line (28) on the lower substrate through the prominent spacer to

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provide a common voltage for every pixel [see fig.8]. However, Kodate lacks disclosure of common electrode pads electrically connected to the common electrodes and each common electrode pads corresponding to one photo spacer, which is mentioned by the Examiner in page 3 of the Office Action above-identified.

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Hirakata et al disclose the internal conducting lines (21) are positioned on the surface of the lower substrate and electrically connected to an external electrode. The internal conducting lines (21) are electrically connected to the relative counter electrode (24) through a plurality of pads (22) and conducting spacers (26) to provide a common voltage for every pixel [see fig.13]. Fig.13 clearly shows that the pads (22) have bowl shapes and are positioned above the internal conducting lines (21). Therefore, Hirakata et al are silent about teaching forming the internal conducting lines and the pads simultaneously and in a same plane, wherein the internal conducting lines and the pads are regarded as the common electrodes and common electrode pads of the present application.

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In addition, Rho never teaches forming common electrode pads, either. Accordingly, Kodate, Rho and Hirakata et al do not teach that a plurality of scan lines, a plurality of common electrodes, and a plurality of common electrode pads are formed on an upper surface of the lower substrate *simultaneously, and never teach forming the common electrode pads and common electrode in a same plane*. Therefore, claims 1 and 9 are believed to be allowable. Accordingly, reconsideration and allowance of the amended claims 1 and 9 are politely requested. Since claims 2-4, 6-8 and claims 10-12, 14-16, are respectively dependent upon the amended claims 1 and 9, they should be allowed if the amended claims 1 and 9 are allowable. Reconsideration of claims 2-4, 6-8, 10-12, and 14-16 are hereby requested.

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2. Rejections of Claims 5 and 13 under 35 U.S.C. 103(a):

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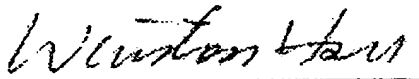
Claims 5 and 13 are rejected under 35 U.S.C. 103(a) for reasons of record, as recited on page 5 of the above-indicated Office action.

Response:

- 5 Since claims 5 and 13 are respectively dependent upon the amended claim 1 and 9, the Applicant believes claims 5 and 13 are accordingly allowable. Therefore, reconsideration and allowance of the claims 5 and 13 are politely requested.

- 10 Applicant respectfully requests that a timely Notice of Allowance be issued in this case.

Sincerely yours,



Date: July 29, 2005

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